

LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING (AUTONOMOUS)

L.B. Reddy Nagar :: Mylavaram-521 230 :: NTR Dist. :: A.P Approved by AICTE, New Delhi. Affiliated to JNTUK, Kakinada

Revised Time Table for Electrical and Electronics Engineering Branch Regular Students only

B.Tech. (III Semester) (R20) Semester End Examinations (Regular) Dec 2023 / Jan 2024

Note: The supplementary examinations (R20) for Electrical **Electronics** Engineering students will be conducted during 19-12-2023 to 30-12-2023 as per the original schedule

Time: 10.00 AM to 01.00 PM

A.Y.: 2023-24

Branch	Date & Day					
	26-12-2023 (Tuesday)	28-12-2023 (Thursday)	30-12-2023 (Saturday)	02-01-2024 (Tuesday)	04-01-2024 (Thursday)	06-01-2024 (Saturday)
Electrical and Electronics Engineering	20EE05 - Electrical Circuit Analysis	20EE06 - Digital Electronics	20EE07 - Electric and Magnetic Fields	20CS03 - Data Structures	20FE10 - Numerical Methods and Integral Calculus	20MC02 - Environmental Science

Note: Any omissions or clashes in the time table may please be informed to the Controller of Examinations immediately.

Date: 15-12-2023

CONTROLLER OF EXAMINATIONS

Copy to: 1. Vice-Principal, Deans & HoDs

2. T&P cell, Transport in-charge & Librarian

3. Canteen, PD, Security & Hostels 4. Coordinator-Disciplinary 5. Notice Boards