

A 3-day Faculty Development program on **“Advanced Digital System Design with FPGAs”**

Date: 2nd April to 4th April 2018.

No. of Participants: 33

Conducted by Electronics and Instrumentation Engineering

Recourse Persons:

1. Mr. Nagendra Kumar, Senior Application Engineer, CoreEL Technologies (Pvt) Ltd, Bangalore.
2. Dr. Y. Amar Babu, Professor, Dept. of ECE, LBRCE.



